



PETER C. SALMON
Peter C. Salmon, LLC
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Mountain View, CA 94041
(650) 814-1076 (mobile)

CAREER PROFILE

Broad operating experience in senior technical roles at Fairchild, Intel, ESL/TRW, GTE, and three startup companies spanning the semiconductor, printing, and defense industries. Author of over 30 patents.

EXPERIENCE

- **Semiconductors**: Designed multiple custom integrated circuits, hybrid assemblies, and manufacturing processes. Developed advanced technology for integrating semiconductors into systems, including two new flip chip connectors, a novel cooling method, a new test methodology, and a solder-free assembly method. Have emphasized the fundamental importance of effective rework strategies for locating and replacing defective components in systems. Wafer and panel level processing, including automation for low cost production.
- **Expert Witness**: Member of Silicon Valley Expert Witness Group and Round Table Group. Ability to quickly understand patents and patent portfolios, and develop written and oral arguments. Provided expert assistance to Howrey, LLP on a major LCD display case involving US, European, and Korean entities. The work included review of approximately 100 patents with claim charts for each, and product tear-downs and testing of television sets, computer monitors, and OEM displays to check for infringement.
- **Systems Design**: Systems Engineer on large military reconnaissance systems, developing hardware and software. Designer of multiple systems, commercial and military.
- **Patent Author**: Learned the art of patent writing from the late Aldo J. Test. Experience relates to technology, prosecution, and associated business development. Author of over 30 patents. In 2007 sold a group of two issued and two un-issued patents to a major Asian manufacturer for \$150,000.
- **Liquid Crystal Displays**: Senior Partner of LCDDisplayConsultants.com.
- **Printers**: Author of 9 printer patents including thermal, electrostatic, and laser printing technologies and printing systems.

- Electrostatic Motors: Currently developing new motor technology based on Coulomb forces, offering higher torque in smaller sizes as well as more energy-efficient operation. Motors and generators are designed to operate at around 10,000 volts.

WORK HISTORY

7/2001 – Present

Peter C. Salmon, LLC

Owner

- Consulting services relating to electrical engineering, electronic packaging and systems design.
- Expert Witness in patent litigation cases, including infringement studies and product tear-downs.

7/2007 – Present

Salmon Technologies, LLC

Vice President

- Created a broad portfolio of patents relating to a standard methodology for integrating semiconductors into electronic systems. Inventions range from a new flip chip connector having high density, high speed, and automated re-workability; to a second flip chip connector for use in solder-free assemblies; to new cooling methods involving copper substrates and chilled water; and a new test method for high speed functional test in miniaturized assemblies.
- 7 new patents issued plus over 25 patent applications in progress
- Developing new technology for electrostatic motors and generators

11/1997 – 6/2001

THE SALMON GROUP, LLC

Founder and CTO

- Invented and partially developed Solid State Printing, SSP, a new method for high performance digital printing that eliminates the optical subsystems of existing laser printers
- Leader of the development group comprising a core team of system engineers plus multiple consultants and suppliers
- Developed a novel semiconductor process for the print heads

6/1991 - 11/1997

TRW

Senior Staff Engineer - Systems

- Led the Engineering Automation Group
- Developed processes for requirements management and thread modeling
- Chaired a working group called Model Driven System Design under INCOSE, the International Council of System Engineers

- Systems engineer on several Reconnaissance Systems
- Generated multiple proposals and system specifications

7/1989 - 6/1991

GTE Government Systems

Section Head

- Managed Reconnaissance Systems Group of 13 engineers
- Co-developed a behavioral model for a \$100M reconnaissance system and used it to drive the system software architecture
- Active in risk management training, OOD, CORBA

6/1986 - 7/1989

ESL, a subsidiary of TRW

Senior Member of the Technical Staff

- Performed in various technical roles such as Systems Engineer and Technical Director on programs from \$12M to \$23M
- Wrote white paper on Search System Performance

4/1984 - 5/1986

Source III

VP Business Development

- Developed library of standard cells for ASIC development
- Negotiated foundry agreements with 7 wafer manufacturers
- Wrote manual for first-time users of cell-based ASIC tools

9/1979 - 2/1984

Santek

Co-founder & VP Engineering

- Developed and patented new thermal printer with integrated dot-driver ICs
- Designed and developed several full-custom ICs
- Negotiated license agreement with Kyocera

4/1974 - 8/1979

Intel

Staff Engineer

- Developed support chips for the 8080 family
- Group leader for engine control circuits
- Developed and patented novel concept for video games
- Taught graduate electronics course at Santa Clara university

6/1971 - 4/1974

Fairchild Semiconductor

Chip Design Engineer

- Logic and circuit designer for several calculator chips

EDUCATION

1972 Northeastern University, Boston
Electrical Engineer, a professional degree
Major: Device Physics; Minor: Materials Science
1969 Northeastern University, Boston
Master of Science in Electrical Engineering
1967 Auckland University, New Zealand
Bachelor of Electrical Engineering

AFFILIATIONS

- Institute of Electrical and Electronic Engineers (IEEE), CPMT chapter
- IEEE CNSV, Consultant's Network of Silicon Valley
- SVEWG, Silicon Valley Expert Witness Group
- RTG, Round Table Group
- Meptec, Micro-Electronic Packaging and Test Engineering Council
- Inventor's Alliance, Bay Area Chapter

